

## **ABSTRACT OF THE DISCLOSURE**

A PAPR reduction method using bit reallocation is disclosed, which is applied in a multi-carrier system. The lowest total transmission power  $P$  is achieved by a bit loading algorithm conditioned on the requirement of 5 total  $D$  transmission bits per block. When the PAPR (peak to average power ratio) of the block is larger than a predetermined value  $A$ , the bit reallocation is performed to add  $\Delta d$ -bit transmitting data to one sub-carrier and subtract  $\Delta d$ -bit transmitting data from another sub-carrier, thereby continuing bit reallocation until the PAPR meets with the system 10 requirement or an iteration number reaches a predetermined maximal number of iteration  $L$ .